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**DIPLOMA COURSE IN INTELLECTUAL PROPERTY RIGHT
LAWS EXAMINATION, 2018
INDUSTRIAL DESIGNS, GEOGRAPHICAL INDICATIONS AND
LAYOUT DESIGNS OF INTEGRATED CIRCUITS
Paper IV**

Time : Three Hours

Maximum Marks : 100

N.B. :— (i) Question No. 8 is compulsory. It carries 20 marks.

(ii) Out of the remaining, attempt any *five* questions, each carrying 16 marks.

1. Discuss the historical evolution of Design Laws in India.
2. Which designs are registrable under Design Act, 2000 ? Explain the procedure for registration of design.
3. Write a detailed note on the Treaty in respect of Integrated Circuits, Washington D.C. 1989.
4. Discuss the composition, powers and functions of regulatory authorities under the Geographical Indications Act, 1999.
5. Explain the provisions relating to protection to certain trademarks under Geographical Indications Act, 1999.

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6. What do you mean by layout design ? Explain the layout designs which can be registered under the Semiconductor Integrated Circuits Layout Design Act, 2000.
7. What is piracy of registered design ? Explain the remedies available to the proprietor of registered designs.
8. Write short notes on any *two* :
 - (a) Prohibition of registration under the Geographical Indications Act, 1999.
 - (b) Difference between Industrial Designs and Semiconductor Integrated Circuits Layout Designs.
 - (c) The procedure of acceptance and opposition to the registration of layout design.
 - (d) Difference between Geographical Indication and Collective Mark.